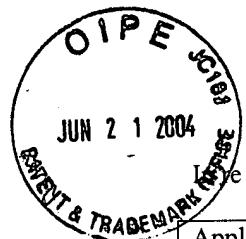


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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**



Leave the application of:

Applicant :	Paolo Menegoli	Docket No :	96-S-119C2
Serial No :	09/659,885	Group No :	2811
Filed :	September 12, 2000	Examiner :	Steven Ho Yin Loke
For :	Method Of Operating A Vertical DMOS Transistor with Schottky Diode Body Structure	Confirmation No :	2853

**TRANSMITTAL LETTER**

Mail Stop Issue Fee  
 Attention: Official Draftsman  
 Commissioner for Patents  
 P. O. Box 1450  
 Alexandria, VA 22313-1450

Sir:

In response to the Notice of Allowability (PTO-37) mailed March 18, 2000, for the above-identified patent application, please find enclosed for filing twelve (12) sheets of formal drawings.

The Commissioner is hereby authorized to charge any fees which may be required to Deposit Account No. 19-1353. A duplicate copy of this sheet is enclosed.

Respectfully submitted

Mario J. Donato, Jr.  
 Reg. No. 37,816  
 Attorney for Applicant

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 972-466-7503

**CERTIFICATE OF MAILING**  
 37 CFR 1.8(a)

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail with sufficient postage, in an envelope addressed to: Mail Stop Issue Fee Attention: Official Draftsman Commissioner for Patents P. O. Box 1450 Alexandria, VA 22313-14501 on the date below:

June 17, 2004  
 Date

Angie Rodriguez  
 Signature